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## » Key

IEEE JNL IEEE Journal or Magazine

IET JNL IET Journal or Magazine

IEEE CNF IEEE Conference Proceeding

IET CNF IET Conference Proceeding

IEEE STD IEEE Standard

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- ☐ 1. **An Adjustable Gain-Clamped Semiconductor Optical Amplifier (AGC-SOA)**  
 Michie, C.; Kelly, A. E.; Armstrong, I.; Andonovic, I.; Tombling, C.;  
[Lightwave Technology, Journal of](#)  
 Volume 25, Issue 6, June 2007 Page(s):1466 - 1473  
 Digital Object Identifier 10.1109/JLT.2007.895533  
[AbstractPlus](#) | Full Text: [PDF\(567 KB\)](#) IEEE JNL  
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- ☐ 2. **Multiwavelength erbium-doped fiber Fabry-Pe/spl acute/rot laser and its i lines power operation**  
 F. Ahmed; N. Kishi; T. Miki;  
[Photonics Technology Letters, IEEE](#)  
 Volume 17, Issue 4, April 2005 Page(s):753 - 755  
 Digital Object Identifier 10.1109/LPT.2004.842796  
[AbstractPlus](#) | [References](#) | Full Text: [PDF\(141 KB\)](#) IEEE JNL  
[Rights and Permissions](#)
- ☐ 3. **Power-line communications channel estimation and tracking by a compe network**  
 Raugi, M.; Tucci, M.;  
[Consumer Electronics, IEEE Transactions on](#)  
 Volume 52, Issue 4, Nov. 2006 Page(s):1213 - 1219  
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- ☐ 4. **Micromechanical gain slope compensator for spectrally linear optical poi**  
 Goosen, K.W.; Walker, J.A.; Neilson, D.T.; Ford, J.E.; Knox, W.H.;  
[Photonics Technology Letters, IEEE](#)  
 Volume 12, Issue 7, July 2000 Page(s):831 - 833  
 Digital Object Identifier 10.1109/68.853515  
[AbstractPlus](#) | [References](#) | Full Text: [PDF\(76 KB\)](#) IEEE JNL  
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- ☐ 5. **TRIAC dimmable ballast with power equalization**  
 Lee, S.T.S.; Henry Shu-Hung Chung; Hui, S.Y.;  
[Power Electronics, IEEE Transactions on](#)  
 Volume 20, Issue 6, Nov. 2005 Page(s):1441 - 1449

Digital Object Identifier 10.1109/TPEL.2005.857560

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- ☐ **6. A nonlinear equalization method based on multilayer perceptron for OFD communication**  
Chunfeng Luo; Shijie Cheng; Lan Xiong; Nguimbis, J.; Youbing Zhang; Jia Ma;  
[Power Delivery, IEEE Transactions on](#)  
Volume 20, Issue 4, Oct. 2005 Page(s):2437 - 2442  
Digital Object Identifier 10.1109/TPWRD.2005.855476  
[AbstractPlus](#) | [References](#) | Full Text: [PDF\(576 KB\)](#) IEEE JNL  
[Rights and Permissions](#)
  
- ☐ **7. Channel noise robust transmit power optimized linear precoder for ISI ch minimal complexity equalizer**  
Man-Wai Kwan; Chi-Wah Kok;  
[Wireless Communications, IEEE Transactions on](#)  
Volume 4, Issue 4, July 2005 Page(s):1461 - 1470  
Digital Object Identifier 10.1109/TWC.2005.850258  
[AbstractPlus](#) | Full Text: [PDF\(304 KB\)](#) IEEE JNL  
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- ☐ **8. Minimum mean square equalization in cyclostationary and stationary inte analysis and subscriber line calculations**  
Petersen, B.R.; Falconer, D.D.;  
[Selected Areas in Communications, IEEE Journal on](#)  
Volume 9, Issue 6, Aug. 1991 Page(s):931 - 940  
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- ☐ **9. Power Allocation in Linear and Tree WSN Topologies**  
Thatte, Gautam; Mitra, Urbashi;  
[Signals, Systems and Computers, 2006. ACSSC '06. Fortieth Asilomar Confer](#)  
Oct.-Nov. 2006 Page(s):1342 - 1346  
Digital Object Identifier 10.1109/ACSSC.2006.354975  
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- ☐ **10. An Extended Kalman Filter Based Decision Feedback Fuzzy Adaptive Eq Power Line Channel**  
Wai Kit Wong; Heng Siong Lim;  
[Power Line Communications and Its Applications, 2006 IEEE International Syr](#)  
26-29 March 2006 Page(s):261 - 266  
[AbstractPlus](#) | Full Text: [PDF\(288 KB\)](#) IEEE CNF  
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- ☐ **11. Multi-Wavelength Lasing and Uniform Spectral Lines Power Operation of Doped Fiber Fabry-Perot Laser**  
Ahmed, F.; Kishi, N.; Miki, T.;  
[Lasers and Electro-Optics, 2005. CLEO/Pacific Rim 2005. Pacific Rim Confere](#)  
30-02 Aug. 2005 Page(s):520 - 521  
[AbstractPlus](#) | Full Text: [PDF\(160 KB\)](#) IEEE CNF  
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- ☐ **12. Wideband LDMOS-based RF linearizer for multi-carrier 3G high power an**  
Chan-Wang Park;  
[Advances in Wired and Wireless Communication, 2004 IEEE/Sarnoff Symposi](#)  
26-27 Apr 2004 Page(s):59 - 62

Digital Object Identifier 10.1109/SARNOF.2004.1302840

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- ☐ **13. On fuzzy-DFE-LMS and fuzzy-DFE-RLS algorithms to equalize power line**  
Ribeiro, M.V.;  
[Industrial Electronics, 2003. ISIE '03. 2003 IEEE International Symposium on](#)  
Volume 2, 9-11 June 2003 Page(s):1001 - 1006 vol. 2

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- ☐ **14. An MPI mitigation technique for high order data modulations in the prese interference**  
Jonghyung Kwun; Hyunkyu Yu; Goohyun Park; Daesik Hong; Changeon Kang  
[Communication Systems, 2002. ICCS 2002. The 8th International Conference](#)  
Volume 1, 25-28 Nov. 2002 Page(s):456 - 459 vol.1

[AbstractPlus](#) | Full Text: [PDF\(330 KB\)](#) IEEE CNF  
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- ☐ **15. A low power CMOS adaptive line equalizer for fast Ethernet**  
Kwisung Yoo; Hoon Lee; Gunhee Han;  
[ASIC, 2002. Proceedings. 2002 IEEE Asia-Pacific Conference on](#)  
6-8 Aug. 2002 Page(s):129 - 132  
Digital Object Identifier 10.1109/APASIC.2002.1031549

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- ☐ **16. Gain slope compensator for spectrally linear optical power equalization**  
Goossen, K.W.; Walker, J.A.; Neilson, D.T.; Ford, J.E.; Knox, W.H.;  
[Lasers and Electro-Optics Society 1999 12th Annual Meeting. LEOS '99. IEEE](#)  
Volume 2, 8-11 Nov. 1999 Page(s):844 - 845 vol.2  
Digital Object Identifier 10.1109/LEOS.1999.811998

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- ☐ **17. Power-efficient coded modulation and precoding schemes for wireless ir communications**  
Hyuncheol Park;  
[Communications, 1999. ICC '99. 1999 IEEE International Conference on](#)  
Volume 1, 6-10 June 1999 Page(s):614 - 618 vol.1  
Digital Object Identifier 10.1109/ICC.1999.768011

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- 18. An Adjustable Gain-Clamped Semiconductor Optical Amplifier (AGC-SOA)**  
Michie C. ; Kelly A. E. ; Armstrong I. ; Andonovic I. ; Tombling C. ;  
[Journal of Lightwave Technology : Accepted for future publication](#)  
Volume PP, Issue 99, 2007 Page(s):1 - 1  
Digital Object Identifier 10.1109/JLT.2006.895533

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<input type="checkbox"/>	L6	L4 and L2	662
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<input type="checkbox"/>	L3	(375/229).ccls. or (375/230).ccls. or (375/231).ccls. or (333/18).ccls. or (333/28r).ccls. or (708/300).ccls. or (708/311).ccls. or (708/312).ccls. or (708/322).ccls. or (708/323).ccls.	5907
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**PALM INTRANET**

## Inventor Information for 10/024606

Inventor Name	City	State/Country
KAKU, TAKASHI	KAWASAKI	JAPAN
OKITA, RYOJI	KAWASAKI	JAPAN

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**PALM INTRANET**
**Inventor Name Search Result**

Your Search was:

Last Name = KAKU

First Name = TAKASHI

Application#	Patent#	Status	Date Filed	Title	Inventor Name
<a href="#">06126527</a>	<a href="#">4317207</a>	150	03/03/1980	DATA TRANSMISSION SYSTEM	KAKU, TAKASHI
<a href="#">06327730</a>	<a href="#">4445224</a>	150	12/04/1981	PULL-IN CIRCUIT FOR A DIGITAL PHASE LOCKED LOOP	KAKU, TAKASHI
<a href="#">06393120</a>	<a href="#">4482973</a>	150	06/28/1982	DIGITAL AUTOMATIC GAIN CONTROL CIRCUIT	KAKU, TAKASHI
<a href="#">06527573</a>	<a href="#">4571733</a>	150	08/17/1983	AUTOMATIC EQUALIZATION DEVICE AND METHOD OF STARTING-UP THE SAME	KAKU, TAKASHI
<a href="#">06589089</a>	Not Issued	166	02/17/1984	TRAINING METHOD OF DATA RECEIVING EQUIPMENT	KAKU, TAKASHI
<a href="#">06645729</a>	<a href="#">4630126</a>	150	08/30/1984	METHOD OF SIGNAL TRANSMISSION WITH SELECTED SIGNAL TRANSMISSION RATE	KAKU, TAKASHI
<a href="#">06770175</a>	<a href="#">4607230</a>	150	08/28/1985	RECEIVER UNIT HAVING SYNCHRONOUS PULL-IN CIRCUIT	KAKU, TAKASHI
<a href="#">06842355</a>	<a href="#">4672630</a>	150	03/18/1986	TRAINING METHOD OF DATA RECEIVING EQUIPMENT	KAKU, TAKASHI
<a href="#">06870499</a>	<a href="#">4868850</a>	150	06/04/1986	MODEM COMMUNICATION SYSTEM HAVING TRAINING MEANS AND METHOD FOR TRAINING SAME	KAKU, TAKASHI
<a href="#">06870546</a>	<a href="#">4694469</a>	150	06/04/1986	METHOD AND DEVICE FOR TIMING PULL-IN OF RECEIVING EQUIPMENT	KAKU, TAKASHI
<a href="#">06944508</a>	<a href="#">4799214</a>	150	12/22/1986	TWO-WIRE FULL DUPLEX FREQUENCY DIVISION MULTIPLEX MODEM SYSTEM HAVING ECHO CANCELLATION MEANS	KAKU, TAKASHI

<u>07327121</u>	Not Issued	166	03/22/1989	DIGITAL SIGNAL PROCESSING SYSTEM IN A MODEM	KAKU, TAKASHI
<u>07512582</u>	<u>5077755</u>	150	04/23/1990	DIGITAL SIGNAL PROCESSING SYSTEM IN MODEM	KAKU, TAKASHI
<u>07582207</u>	<u>5189684</u>	150	10/02/1990	LINE SIGNAL DETERIORATION REMOVING SYSTEMS	KAKU, TAKASHI
<u>07585622</u>	<u>5175745</u>	150	09/20/1990	TRANSVERSAL TYPE AUTOMATIC EQUALIZER WITH TAP COEFFICIENT PROTECTION	KAKU, TAKASHI
<u>07635520</u>	<u>5319650</u>	250	02/13/1991	MODULATOR- DEMODULATOR DEVICE CAPABLE OF DETECTING AN UNSYNCHRONIZED FRAME STATE	KAKU, TAKASHI
<u>08004762</u>	Not Issued	166	01/14/1993	MULTIPLEX TRANSMISSION SYSTEM WHEREIN ANALOG SIGNAL IS TRANSFORMED TO BASE BAND, RANDOM- TRANSFORMED AND SUPERIMPOSED ON DISPERSED SIGNAL POINTS IN VECTOR SIGNAL SPACE	KAKU, TAKASHI
<u>08009103</u>	<u>5570390</u>	150	01/26/1993	TIMING GENERATION DEVICE AND DATA TRANSMISSION DEVICE HAVING THE TIMING GENERATION DEVICE	KAKU, TAKASHI
<u>08009108</u>	<u>5442656</u>	150	01/26/1993	TIMING EXTRACTION DEVICE AND DATA TRANSMISSION DEVICE USING THE TIMING EXTRACTION DEVICE	KAKU, TAKASHI
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<u>08029238</u>	<u>5557632</u>	150	03/10/1993	DEMODULATION AND ROLL- OFF FILTER APPARATUS	KAKU, TAKASHI
<u>08031621</u>	<u>5583887</u>	150	03/15/1993	TRANSMISSION SIGNAL PROCESSING APPARATUS	KAKU, TAKASHI
<u>08034817</u>	Not Issued	166	03/19/1993	FIXED EQUALIZER AND EQUALIZING METHOD	KAKU, TAKASHI
<u>08053803</u>	<u>5574737</u>	150	04/29/1993	MODULATOR- DEMODULATOR DEVICE	KAKU, TAKASHI



				CAPABLE OF DETECTING AN UNSYNCHRONIZED FRAME STATE BASED ON HARD AND SOFT ERROR VALUES	
<u>08053804</u>	<u>5572537</u>	150	04/29/1993	MODULATOR- DEMODULATOR DEVICE CAPABLE OF DETECTING AN UNSYNCHRONIZED FRAME STATE BASED ON HARD AND SOFT ERROR VALUES	KAKU, TAKASHI
<u>08089190</u>	<u>5631923</u>	150	07/12/1993	TWO-WIRE FULL-DUPLEX MODEM	KAKU, TAKASHI
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<u>08108549</u>	Not Issued	166	08/19/1993	MODULATION AND DEMODULATION SYSTEM USING SPECIAL TRAINING PATTERN	KAKU, TAKASHI
<u>08109044</u>	<u>5448595</u>	250	08/19/1993	AN AUTOMATIC GAIN CONTROL CIRCUIT FOR A DEMODULATION SECTION OF A MODEM	KAKU, TAKASHI
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<u>08240093</u>	Not Issued	166	05/09/1994	MULTIPLEX TRANSMISSION SYSTEM WHEREIN ANALOG SIGNAL IS TRANSFORMED TO BASE BAND, RANDOM- TRANSFORMED AND SUPERIMPOSED ON	KAKU, TAKASHI

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<u>08415926</u>	<u>5734681</u>	250	04/03/1995	PROCESS AND SYSTEM FOR TRANSFERRING VECTOR SIGNAL WITH PRECODING FOR SIGNAL POWER REDUCTION	KAKU, TAKASHI
<u>08441014</u>	<u>5598433</u>	150	05/15/1995	AUTOMATIC EQUALIZER AND DATA MODE CONVERGENCE METHOD	KAKU, TAKASHI
<u>08468186</u>	<u>5650953</u>	150	06/06/1995	RECIPROCAL NUMBER ARITHMETIC OPERATING METHOD AND CIRCUIT WHICH ARE USED IN MODEM	KAKU, TAKASHI
<u>08472291</u>	<u>5563908</u>	150	06/07/1995	MODULATOR AND DEMODULATOR APPARATUS	KAKU, TAKASHI
<u>08482742</u>	<u>5710754</u>	150	06/07/1995	MULTIPLEX TRANSMISSION SYSTEM WHEREIN ANALOG SIGNAL IS TRANSFORMED TO BASE BAND RANDOM- TRANSFORMED AND SUPERIMPOSED ON DISPERSED SIGNAL POINTS IN VECTOR SIGNAL SPACE	KAKU, TAKASHI
<u>08505479</u>	<u>5537437</u>	150	07/21/1995	INITIALIZATION EQUALIZATION FOR MODULATION AND DEMODULATION USING SPECIAL TRAINING PATTERN	KAKU, TAKASHI
<u>08524835</u>	<u>5598434</u>	150	09/07/1995	AUTOMATIC EQUALIZER WITH A BRANCHED INPUT FOR IMPROVED ACCURACY	KAKU, TAKASHI
<u>08529415</u>	<u>5974090</u>	250	09/18/1995	SPECIAL EYE PATTERN AND MODULATION AND DEMODULATION METHOD USING THE SAME	KAKU, TAKASHI
<u>08534161</u>	<u>5719907</u>	250	09/26/1995	PHASE JITTER EXTRACTION CIRCUIT AND PHASE JITTER CANCELLATION CIRCUIT	KAKU, TAKASHI
<u>08539067</u>	<u>6021160</u>	150	10/04/1995	TRAINING METHOD FOR NON-NYQUIST TRANSMISSION SYSTEM AND TRAINING DATA TRANSMISSION APPARATUS	KAKU, TAKASHI

				FOR NON-NYQUIST TRANSMISSION SYSTEM	
<a href="#">08543056</a>	<a href="#">6154490</a>	150	10/13/1995	SIGNAL PROCESSING APPARATUS	KAKU, TAKASHI
<a href="#">08547614</a>	<a href="#">5987064</a>	150	10/24/1995	EYE PATTERN DISPLAY METHOD, EYE PATTERN DISPLAY APPARATUS, AND COMMUNICATIONS APPARATUS	KAKU, TAKASHI
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<a href="#">08629382</a>	<a href="#">5825818</a>	150	04/08/1996	APPARATUS AND METHOD OF RECOVERING A TIMING SIGNAL IN A TRANSMISSION APPARATUS	KAKU, TAKASHI
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<a href="#">08748346</a>	<a href="#">6002724</a>	150	11/14/1996	TRANSMISSION APPARATUS AND CONTROL METHOD THEREFOR	KAKU, TAKASHI

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**PALM INTRANET**
**Inventor Name Search Result**

Your Search was:

Last Name = OKITA

First Name = RYOJI

Application#	Patent#	Status	Date Filed	Title	Inventor Name
<u>06645729</u>	<u>4630126</u>	150	08/30/1984	METHOD OF SIGNAL TRANSMISSION WITH SELECTED SIGNAL TRANSMISSION RATE	OKITA, RYOJI
<u>07796421</u>	<u>5406564</u>	250	11/22/1991	COMMUNICATION LINE BACKUP SYSTEM	OKITA, RYOJI
<u>08004762</u>	Not Issued	166	01/14/1993	MULTIPLEX TRANSMISSION SYSTEM WHEREIN ANALOG SIGNAL IS TRANSFORMED TO BASE BAND, RANDOM-TRANSFORMED AND SUPERIMPOSED ON DISPERSED SIGNAL POINTS IN VECTOR SIGNAL SPACE	OKITA, RYOJI
<u>08235652</u>	Not Issued	166	04/29/1994	DATA TRANSMISSIONS SYSTEM HAVING BACKUP TESTING FACILITY	OKITA, RYOJI
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<u>08482742</u>	<u>5710754</u>	150	06/07/1995	MULTIPLEX TRANSMISSION SYSTEM WHEREIN ANALOG SIGNAL IS TRANSFORMED TO BASE BAND RANDOM-TRANSFORMED AND SUPERIMPOSED ON DISPERSED SIGNAL POINTS IN VECTOR SIGNAL SPACE	OKITA, RYOJI
<u>08524835</u>	<u>5598434</u>	150	09/07/1995	AUTOMATIC EQUALIZER WITH	OKITA, RYOJI

				A BRANCHED INPUT FOR IMPROVED ACCURACY	
<u>08624055</u>	<u>5689513</u>	150	03/29/1996	DATA TRANSMISSION SYSTEM HAVING A BACKUP TESTING FACILITY	OKITA, RYOJI
<u>08629382</u>	<u>5825818</u>	150	04/08/1996	APPARATUS AND METHOD OF RECOVERING A TIMING SIGNAL IN A TRANSMISSION APPARATUS	OKITA, RYOJI
<u>08748346</u>	<u>6002724</u>	150	11/14/1996	TRANSMISSION APPARATUS AND CONTROL METHOD THEREFOR	OKITA, RYOJI
<u>08749346</u>	<u>5896420</u>	250	11/21/1996	TRANSMISSION APPARATUS HAVING ECHO CANCELLATION FACILITY	OKITA, RYOJI
<u>08833260</u>	<u>6044109</u>	150	04/04/1997	MODEM SIGNAL TRANSMITTER, MODEM SIGNAL TRANSMITTING METHOD, MODEM SIGNAL RECEIVER, MODEM SIGNAL RECEIVING METHOD, MODEM SIGNAL TRANSMITTING/RECEIVING SYSTEM AND MODEM SIGNAL TRANSMITTING/RECEIVING METHOD	OKITA, RYOJI
<u>08916595</u>	<u>6298036</u>	150	08/22/1997	MULTIPLEX TRANSMISSION SYSTEM WHEREIN ANALOG SIGNAL IS TRANSFORMED TO BASE BAND, RANDOM-TRANSFORMED AND SUPERIMPOSED ON DISPERSED SIGNAL POINTS IN VECTOR SIGNAL SPACE.	OKITA, RYOJI
<u>08953649</u>	<u>6052410</u>	150	10/17/1997	TRANSMISSION APPARATUS, RECEPTION APPARATUS AND COMMUNICATION APPARATUS AS WELL AS MODEM SIGNAL TRANSMISSION AND RECEPTION METHOD	OKITA, RYOJI
<u>08956553</u>	<u>6282246</u>	150	10/23/1997	FREQUENCY MODULATION METHOD AND MODEM UNIT EMPLOYING SUCH METHOD	OKITA, RYOJI
<u>09164750</u>	<u>6226302</u>	150	10/01/1998	MULTIPLEX TRANSMISSION SYSTEM WHEREIN ANALOG SIGNAL IS TRANSFORMED TO BASE BAND, RANDOM-TRANSFORMED AND	OKITA, RYOJI

				SUPERIMPOSED ON DISPERSED SIGNAL POINTS IN VECTOR SIGNAL SPACE	
<u>09167612</u>	<u>6282237</u>	150	10/06/1998	MULTIPLE TRANSMISSION SYSTEM WHEREIN ANALOG SIGNAL IS TRANSFORMED TO BASE BAND, RANDOM- TRANSFORMED AND SUPERIMPOSED ON DISPERSED SIGNAL POINTS IN VECTOR SIGNAL SPACE	OKITA, RYOJI
<u>09192429</u>	<u>6411650</u>	250	11/16/1998	PLL CONTROL METHOD IN DATA RECEIVING APPARATUS	OKITA, RYOJI
<u>09455000</u>	Not Issued	161	12/06/1999	TRANSMISSION APPARATUS, RECEPTION APPARATUS AND COMMUNICATION APPARATUS AS WELL AS MODEM SIGNAL TRANSMISSION AND RECEPTION METHOD	OKITA, RYOJI
<u>09833963</u>	Not Issued	161	04/12/2001	Transmission apparatus and method of signal-point generation	OKITA, RYOJI
<u>09844812</u>	Not Issued	164	04/27/2001	FREQUENCY MODULATION METHOD AND MODEM UNIT EMPLOYING SUCH METHOD	OKITA, RYOJI
<u>10024606</u>	Not Issued	71	10/30/2001	Equalization processing method and the apparatus for periodic fluctuation of transmission line characteristic	OKITA, RYOJI
<u>10047132</u>	<u>7113557</u>	150	01/15/2002	NOISE CANCELING METHOD AND APPARATUS	OKITA, RYOJI
<u>10109370</u>	<u>6968497</u>	150	03/28/2002	ERROR CORRECTION DEVICE AND ERROR CORRECTION METHOD	OKITA, RYOJI
<u>10306846</u>	<u>7114120</u>	150	11/25/2002	DATA PROCESSING APPARATUS AND DATA PROCESSING METHOD	OKITA, RYOJI

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